# Ceremonial Kick-off

# Intel Collaborative Research Institute for Autonomous & Resilient Systems (ICRI-CARS)

When: Tuesday, October 17th, 2017
9:30 h – 18:00 h
Where: Georg Christoph Lichtenberg-Haus
Dieburger Straße 241
64287 Darmstadt

1. Agenda
2. Venue
3. Registration
4. Hotels
5. Speakers

Latest updates: www.icri-sc.org/icri-cars-kickoff/welcome/

# Agenda

| | **Tuesday, October 17th** |
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| **TIME** | **Lichtenberg-Haus Darmstadt** |
| 9:30 - 10:00 | **Welcome Coffee** |
| 10:00 - 10:15 | *Welcoming*<br>**Prof. Ahmad-Reza Sadeghi** (TU Darmstadt, Academic Director ICRI-CARS)<br>**Anand Rajan** (Director Emerging Security Labs, Intel Labs, USA) |
| 10:15 - 11:15 | *Internet Authentication Solutions, Old and New*<br>**Prof. Paul van Oorschot** (Carleton University, Canada) |
| ***11:15 - 11:45*** | **Coffee Break** |
| 11:45 - 12:30 | *Industrial Keynote*<br>**Dr. Riccardo Mariani** (Intel Fellow and Chief Functional Safety Technologist) |
| ***12:30 - 13:30*** | **Lunch + Networking** |
| 13:30 - 13:45 | *Welcoming by TU Darmstadt*<br>**Prof. Hans-Jürgen Prömel** (President TU Darmstadt) |
| 13:45 - 14:15 | *Collaborative driving automation – dependability challenges*<br>**Dr. Astrid Elbe** (Managing Director Intel Labs Europe) |
| ***14:15 - 14:45*** | **Coffee Break** |
| 14:45 - 15:05 | *Trusted Platforms and Analytics Research*<br>**Prof. N. Asokan** (Aalto University) |
| 15:05 - 15:45 | *Embedded Systems Security Research*<br>**Prof. Christof Paar** (Ruhr-Universität Bochum)<br>**Prof. Thorsten Holz** (Ruhr-Universität Bochum)<br>**Prof. Tim Güneysu** (Ruhr-Universität Bochum) |
| 15:45 - 16:05 | *Resilient Distributed Systems Research*<br>**Prof. Paulo Esteves-Veríssimo** (University of Luxembourg) |
| 16:05 – 16:15 | *A Multi-Faceted Approach to Functional Safety*<br>**Prof. Andreas Steininger** (TU Wien) |
| **16:15 – 18:00** | **Closing + Get Together** |

# Venue

The Ceremonial Kick-off will take place at Lichtenberg-Haus in Darmstadt, Germany.

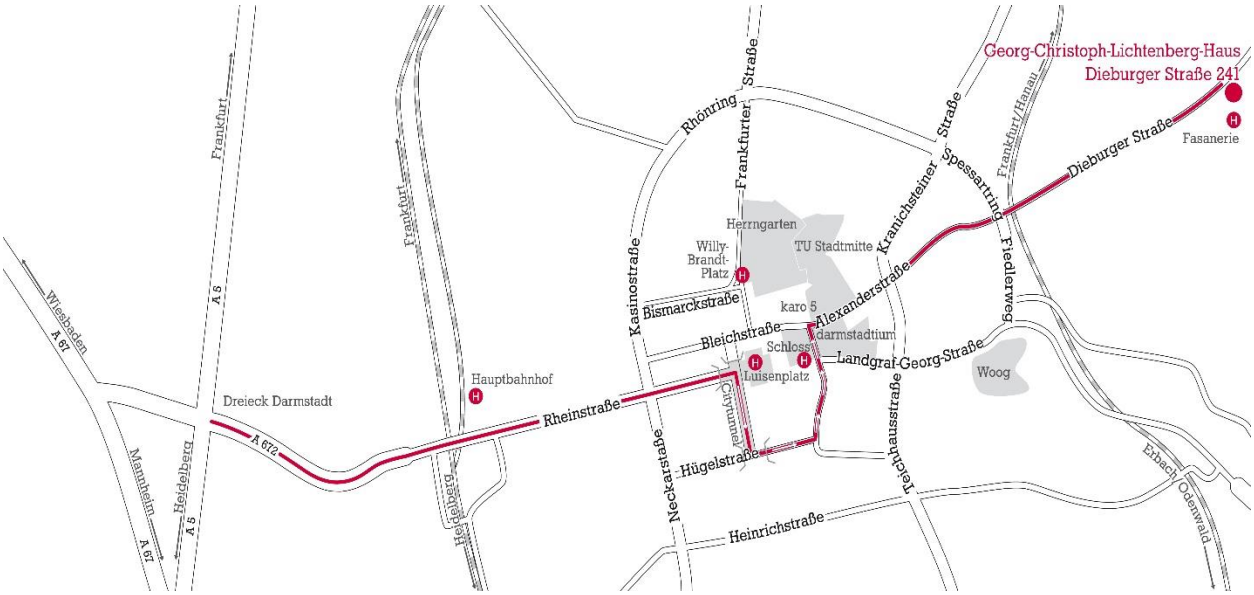
Georg Christoph Lichtenberg-Haus
Dieburger Straße 241
64287 Darmstadt

... by car:

You can reach Darmstadt using the A5 Autobahn (from Frankfurt/Main or Heidelberg/Basel) and the A67 Autobahn (from Cologne/Wiesbaden or Mannheim), which intersect 3 km to the west of the city at the intersection "Autobahnkreuz Darmstadt". The TUD locations "Stadtmitte" (city center) and "Lichtwiese" are well marked within the entire city.
After leaving the Autobahn in the direction of Darmstadt, drive along the Rheinstrasse towards the city center (Stadtmitte) following the signs for "TU Stadtmitte". Drive straight on into the Cityring-Tunnel. After bearing right as you enter the tunnel, exit the tunnel to the left on Hügelstrasse.
At the next light, turn left again on to Kirchstraße. Go straight through the next light and you will pass the Darmstadt castle (Schloss) on your left. At the next light, turn right on Alexanderstraße, which turns into Dieburger Straße further up the road. Georg Christoph Lichtenberg-Haus is located on the right hand side of Dieburger Straße. The house number is 241.

... by train:

Darmstadt is an ICE-Station and stop for IC, EC and Interregio trains travelling north-south. Regionally Darmstadt is connected to the Frankfurt/Main-Heidelberg or Mannheim, Wiesbaden/Mainz-Aschaffenburg and Darmstadt-Erbach or Eberbach (Odenwald) lines.
To reach Georg Christoph Lichtenberg-Haus, you can take Bus F from the main train station (Hauptbahnhof) to the bus stop "Fasanerie". The bus stop is directly in front of the house.



# Registration

The workshop is invitation only and registration is mandatory due to limited places.

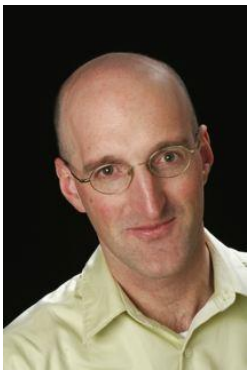
If you would like to attend the Ceremonial Kick-off, please send a short Email with your name and affiliation to: office(at)trust.tu-darmstadt.de

# Hotels

Due to many fairs and conferences, we recommend to book your hotel as soon as possible. The following hotels have bookable rooms left upon request and availability:

Welcome Hotel
Maritim Hotel
InterCity Hotel
Hotel Bockshaut
H-Hotels
Contel Hotel

# Speakers



**Prof. Paul Van Oorschot** is a Professor of Computer Science at Carleton University in Ottawa, where he is Canada Research Chair in Authentication and Computer Security. He is an ACM Fellow, and a Fellow of the Royal Society of Canada (FRSC), Canada's national academy. He was Program Chair of NSPW 2014-2015, USENIX Security 2008, NDSS 2001-2002, and co-author of the Handbook of Applied Cryptography (1996). He has served on the editorial boards of IEEE TDSC, IEEE TIFS, and ACM TISSEC, and as Scientific Director of NSERC ISSNet (2008-2013), a pan-Canadian strategic research network exploring computer and Internet security. His research interests include authentication and identity management, computer security, Internet security, security and usability, software security, and applied cryptography.

**Dr. Riccardo Mariani** is an Intel Fellow and the chief functional safety technologist in the Internet of Things Group at Intel Corporation. Based in Pisa, Italy, he is responsible for defining strategies, roadmaps and technologies for Internet of Things applications that require functional safety and high performance, including transportation and industrial systems. He is also the functional safety global domain lead for Intel's CISA Architecture Working Model Initiative.

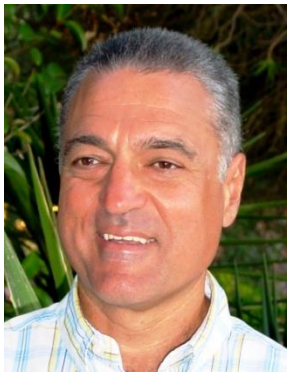
**Dr. Astrid Elbe** is Managing Director of Intel Labs Europe headquartered at Intel's Ireland Campus in Leixlip, Co. Kildare, with additional offices in London, U.K. and Munich, Germany. The organisation is focused on Edge Computing Research with a particular emphasis on Dependable Cyber Physical Systems. Prior to taking her present role in 2016, Astrid held a number of R&D and Engineering Management roles within Intel Product Divisions. She holds a PhD in Surface Physics and has more than 20 patents in areas including cryptography and microarchitecture.

**Prof. N. Asokan** is a professor of Computer Science at Aalto University and University of Helsinki. His research interests are in systems security. He is the lead academic PI of Intel Collaborative Research Center (http://www.icri-sc.org) in Finland and is the director of Helsinki-Aalto Center for Information Security (http://haic.aalto.fi). More information about him and his research at http://asokan.org/asokan/.

**Prof. Christof Paar** has the Chair for Embedded Security at Ruhr University Bochum and is affiliated professor at the University of Massachusetts Amherst. He co-founded CHES (Cryptographic Hardware and Embedded Systems), the leading international conference on applied cryptography. Christof's research interests include efficient crypto implementations, hardware security, and security analysis of real-world systems. He also works on applications of embedded security, e.g., in cars or consumer devices. He holds an ERC Advanced Grant in hardware security and is spokesperson for the doctoral research school SecHuman. Christof has over 180 peer-reviewed publications and he is co-author of the textbook Understanding Cryptography. He is Fellow of the IEEE and was recipient of an NSF CAREER Award, the German IT Security Award and the Innovation Prize NRW. He has given numerous invited talks, including presentations at MIT, Yale, Stanford, IBM Research and Intel.

**Prof. Thorsten Holz** is a professor in the Faculty of Electrical Engineering and Information Technology at Ruhr-University Bochum, Germany. His research interests include systems-oriented aspects of secure systems, with a specific focus on applied computer security. Currently, his work concentrates on bots/botnets, automated analysis of malicious software, and studying latest attack vectors. He received the Dipl.-Inform. degree in Computer Science from RWTH Aachen, Germany (2005), and the Ph.D. degree from University of Mannheim (2009). Prior to joining Ruhr-University Bochum in April 2010, he was a postdoctoral researcher in the Automation Systems Group at the Technical University of Vienna, Austria. In 2011, Thorsten received the Heinz Maier-Leibnitz Prize from the German Research Foundation (DFG).

**Tim Güneysu** is professor and head of the chair for Security Engineering at Ruhr-Universität Bochum, Germany. He is also affiliated with the Cyber Physical Systems (CPS) division of the German Research Center for Artificial Intelligence (DFKI) in Bremen. Tim's primary research topics are in the secure design and implementation of (embedded) systems, including aspects such as long-term secure cryptographic implementation, the design of security layers/architectures and related aspects of hardware-based security. Tim published and contributed to more than 95 peer-reviewed journal and conference publications in the area of applied security and cryptography.



**Prof. Paulo Esteves-Veríssimo** is a Professor and FNR PEARL Chair at the University of Luxembourg Faculty of Science, Technology and Communication (FSTC), since fall 2014, and head of the CritiX lab (Critical and Extreme Security and Dependability) at SnT, the Interdisciplinary Centre for Security, Reliability and Trust at the same University. He is adjunct Professor of the ECE Dept., Carnegie Mellon University. Previously, he has been a Professor of the Univ. of Lisbon, member of the Board of the same university and Director of LaSIGE (http://lasige.di.fc.ul.pt). Veríssimo is Fellow of the IEEE and Fellow of the ACM, and he is associate editor of the IEEE Transactions on Computers (TC - 2015--). He is currently Chair of the IFIP WG 10.4 on Dependable Computing and Fault-Tolerance and vice-Chair of the Steering Committee of the IEEE/IFIP DSN conference. He is currently interested in secure and dependable distributed architectures, middleware and algorithms for: resilience of large-scale systems and critical infrastructures, privacy and integrity of highly sensitive data, and adaptability and safety of real-time networked embedded systems. He is author of over 180 peer-refereed publications and co-author of 5 books. Google Scholar Citations profile.

**Prof. Andreas Steininger** obtained his Master in Electrical Engineering (1988) and his PhD (1993) from TU Wien, where he is currently working as an associate professor for Computer Engineering. His research covers multiple aspects of dependable computing, ranging from fault-tolerance assessment by fault injection over built-in self test to radiation tolerance. The current focus of his research group is on asynchronous computing, clock-domain interfacing and metastability. Steininger has published over 150 journal and conference papers, served as PC Chair and General Chair of several scientific conferences in the area. Apart from his involvement in many national and international fundamental research projects, he has always been in cooperation with industrial partners, which also yielded more than 10 patents in which he is co-inventor. Steininger is significantly contributing to the teaching of Computer Engineering at TU Wien, and has given several invited lectures at international universities and summer schools. He has supervised more than 20 PhD students, and serves as the director of the Vienna PhD School of Informatics.

**Anand Rajan** is the Senior Director of the Emerging Security Lab at Intel Labs. He leads a team of researchers whose mission is to investigate novel security features that raise the assurance of platforms across the compute continuum (Cloud to Wearables). The topics covered by his team span Trustworthy Execution Environments (TEE), IoT & Mobile Security, Cryptography, and Security for Emerging Paradigms (e.g. Autonomous Systems, 5G). Anand is a Principal Investigator for Intel's research collaboration with academia, government, and commercial labs on Trustworthy Platforms. He is the mentor for the Security Research Sector of Intel's Corporate Research Council. Anand was an active member of the IEEE WG that crafted the P1363 (public-key crypto) standard. Anand and team developed the Common Data Security Architecture specification that was adopted as a worldwide standard by The Open Group. His team was also instrumental on several security standardization efforts (e.g. PKCS#11, BioAPI, UPnP-Security, & EPID). Prior to joining Intel in 1994, Anand was technical lead for the Trusted-UNIX team at Sequent Computer Systems and worked on development and certification of a TCSEC B1-level Operating System.